



e-ISSN: 2278-8875  
p-ISSN: 2320-3765

# International Journal of Advanced Research

in Electrical, Electronics and Instrumentation Engineering

Volume 11, Issue 6, June 2022

**ISSN** INTERNATIONAL  
STANDARD  
SERIAL  
NUMBER  
INDIA

**Impact Factor: 8.18**

☎ 9940 572 462

☎ 6381 907 438

✉ [ijareeie@gmail.com](mailto:ijareeie@gmail.com)

@ [www.ijareeie.com](http://www.ijareeie.com)



# Harmonic Analysis of Traction Power Supply Substation Medium Voltage Line

Deepak Shrivastava<sup>1</sup>, Shiv Kumar Tripathi<sup>2</sup>

PG Scholar, Department of Electrical & Electronics, Corporate Institute of Science & Technology,  
Bhopal, India<sup>1</sup>

Asst. Professor, Department of Electrical & Electronics, Corporate Institute of Science & Technology,  
Bhopal, India<sup>2</sup>

**ABSTRACT:** Modern DC electric railway traction power supply system consists of a twelve-pulse rectifier which will cause 11th and 13th order harmonics to be injected into the power system. A shunt active harmonic filter (SAHF) is proposed to mitigate the harmonics at medium voltage (MV) line. SAHF control strategy usually consists of three core elements which include reference current generation, DC-link feedback control, and current control and gate signal generation. This paper proposed a distinct time domain-based control strategy with DC-linked feedback reference current compensation method which has a simple topology, low mathematical complexity, and fast response time. Detailed performance analysis of SAHF is carried out in the parameter selection process whereby the effects of each parameter are discussed. The implementation of SAHF in traction power supply substation (TPSS) is simulated using MATLAB/Simulink.

**KEYWORDS:** shunt active power filter, voltage source converters, harmonics, High Voltage Direct Current (HVDC), transmission systems, Power quality

## I. INTRODUCTION

transportation makes the service convenient, fast and environmentally friendly. However, this also brought a new challenge to the power grid. Especially rapid development in the power electronics technology and the increase in the application of these devices in the railway sector exposed the traction power supply system to a distorted waveform that affects the overall performance of the network due to harmonic currents injected into the system by non-linear loads. This current flow into the surrounding grid through the transmission lines and causes power quality problems that cannot be ignored. It is noteworthy that when there is a resonance into the system, the harmonic current amplified significantly, which in turn disturbs the neighboring lines of communication and the railway signaling system [1]. In addition, it causes overheating, instability of the power capacitors, and brings malfunction of protection devices. Therefore, the harmonic current flow must be assessed accurately in the design and planning stage of the electric traction system [2]. It also needs to be precisely modelled to analyze and assess the harmonic effect on the power-feeding system [3]. One such industry is the electric railway industry. Modern electric railways are powered via DC supply so AC to DC converters are required to convert AC supply from utilities to DC supply. The railway system started with the use of steam power whereby, in 1804, the construction of the first full-scale railway steam locomotive by Richard Trevithick was completed in the United Kingdom [1]. Then, in 1881, Werner von Siemens constructed the first electric tram line in Lichterfelde, Berlin, Germany [2]. This marks the start of the electrification of railways. In 1904, DC traction motor powered by transformer and rotary converter started to be utilized on the Seebach-Wettingen line of the Swiss Federal Railways. The development of high-speed railways by the French TGV and the Japanese Shinkansen in the 1980s promoted another round of railway electrification globally [3]. Containing thyristor rectifiers used for controlling speed in electric railways, they have a non-linear characteristic which leads to current harmonic distortion (CHD). Considering system impedance, such CHD leads to harmonic voltage distortion (VHD). From electric grid viewpoint, the electric railway is a non-linear variable load and is considered as one of the most unfavorable loads [1,2]. In addition to high reactive power consumption, electric railway injects an enormous amount of harmonic current which is almost equal to 2-3 times of allowable system harmonic level [3]. Harmonics induce plenty of troubles such as excess loss in system, unsuitable function of control systems and improper trips of relays. In order to compensate



reactive power and eliminate harmonics, a proper filter must be applied. There are three kinds of filters: passive, active and hybrid filter which is a combination of active and passive filters. Each of passive and active filters has its own pros and cons. However, hybrid filters have advantages of both active and passive filters and on the other hand, few disadvantages in comparison to these two filters. Hybrid filters have different structures with regard to their connections. Each of these structures has its own advantages. There are various methods to eliminate harmonics. One of the methods is through the use of power harmonic filters. The filters can be classified into passive, active, and hybrid filters. This paper will focus on active harmonic filter (AHF) due to its ability to remove all unwanted harmonics present in the system which, unlike passive harmonic filters, each filter can only remove certain targeted harmonics. The basic concept of AHF has first appeared in 1969 as described in [6]. Then, in 1976, the basic design of modern AHF was proposed in [7]. However, it was only until the 1990s where high-power and high-switching speed power electronics became available and allow the practical usage of AHF in power systems [8]. This paper presents the performance analysis of medium voltage SAHF with VSC via parameter testing to determine optimal SAHF configuration to mitigate harmonics generated by the electric railway system in Malaysia in compliance with IEEE std. 519-2014. Besides that, a proposed reference current generation method for the SAHF control strategy which has a simple topology and consumes minimal computational power is discussed. Implementation of DC-link feedback control to compensate reference current is also proposed. The study is in anticipation of the future expansion of the railway system. So, this paper will serve as an important reference for harmonic mitigation in the near future.

## II. METHODOLOGY OF PROPOSED SYSTEM

For mitigating the harmonic distortion passive filtering is the simplest conventional solution. The most commonly used passive filter is the single-tuned filter. This filter is simple and least expensive as compared with other means for mitigating the harmonic problems. Low cost is a great benefit of these filters. These filters are always connected in parallel across the network. The model shown in Fig. 1 is a simplified closed-loop model of the studied urban DC electric railway TPSS system in the form of a typical twelve-pulse system. The model is constructed via MATLAB/Simulink. The system is based on a DC traction railway in Malaysia which operates at 750V DC. The system is supplied with 33kV AC, 50Hz by the traction power supply substation (TPSS) provided by the public utility at the MV side.

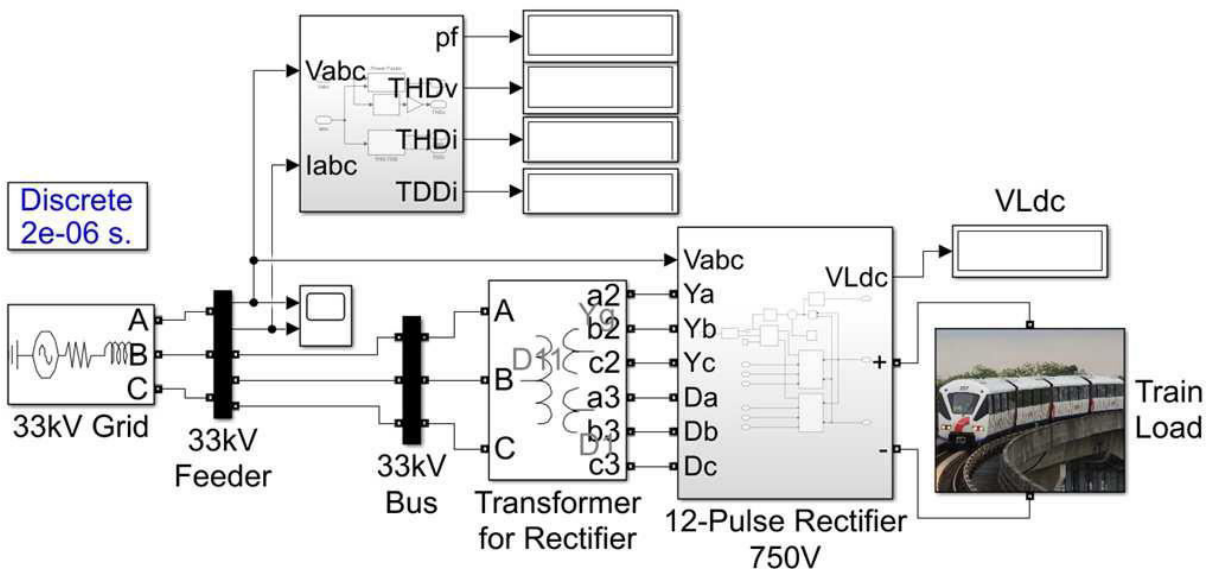


Figure 1: DC electric railway TPSS model

A 3300kVA three-winding rectifier transformer is used to step down the power supply from 33kV AC to 585V AC at the low voltage (LV) side. The transformer consists of one primary winding and two secondary windings which are separately named secondary and tertiary winding. The primary winding is in delta connection which is leading. The secondary winding is in wye connection. The tertiary winding is in delta connection which is lagging. Each of the two three-phase thyristor-based rectifiers is connected to the secondary and tertiary winding of the rectifier transformer respectively. Then, the two rectifiers are connected in parallel. A resistive load, representing the train load, is connected to the rectifiers. The rectifiers are controlled via the use of a twelve-pulse generator. Due to the varying load conditions,



a closed-loop control method is applied to generate a suitable firing angle for the pulse generator using a PI controller to maintain the load voltage at 750V DC as shown in Fig. 2 with proportional ( $K_p$ ) and integral gain ( $K_i$ ) of 0.3 and 50 respectively. The output saturation has been set to limit the alpha firing angle to between 0 to 120.

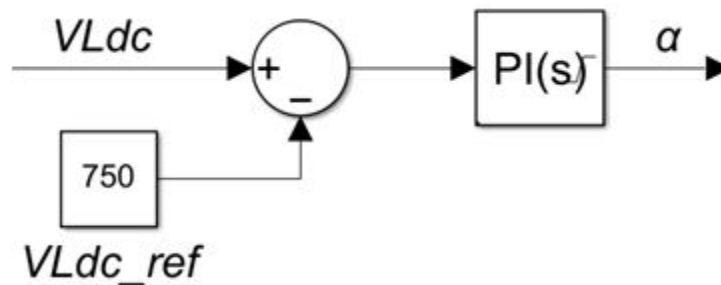


Fig. 2. Closed-loop control model

### III. HARMONIC FILTERING

We used in this article passive filters which are a classic method for power quality improvement, consist of series LC tuned for removing a specific harmonic or blocking a bandwidth of severe harmonics of nonlinear loads current. For mitigating the harmonic distortion passive filtering is the simplest conventional solution. The most commonly used passive filter is the single-tuned filter. This filter is simple and least expensive compared with other means for mitigating the harmonic problems [10]. These filters have low impedances for the tuned frequencies such as 11<sup>th</sup> and 13<sup>th</sup> (used in our application). Low cost is a great benefit of these filters. These filters are always connected in parallel across the network. The harmonic analysis of the railway system model without SAHF is carried out via MATLAB/Simulink FFT analysis tool. The sampled window size is 10 cycles for a 50Hz power system. It is generally known that the twelve-pulse rectifier of the modeled railway system will cause 11<sup>th</sup> and 13<sup>th</sup> order harmonics [13]. Therefore, 11<sup>th</sup> and 13<sup>th</sup> order harmonics will be the main individual harmonics to be mitigated in this paper. Since harmonic distortions are more prominent in off-peak than peak load conditions, only off-peak load conditions supply voltage and current waveform and FFT analysis are shown. These filters would normally be tuned below the respective characteristic frequencies. This is done for the following practical reasons.

- Perfect tuning would attract the dominant harmonics of the neighboring non-linear loads and result in overcurrent condition in the filter and fail.
- Filter components, in particular, the capacitor C parameter decreases due to ageing, and the tuning frequency moves upwards and design at or above the tuning frequency would result in degraded filter performance. With lower frequency detuning, the series resonance frequency increases and shift the minimum impedance point closer to the harmonic frequency. This increases the effectiveness of the filters by suppressing more current harmonics.
- Lower frequency detuning may be necessary to move the parallel resonance frequency away from the dominant harmonic frequency. Depending on the line impedance parameters, this may be necessary to avoid large overvoltage stresses on the rectifier terminals due to parallel resonance at the discussed harmonic frequency.

### IV. COMPENSATED SHUNT ACTIVE HARMONIC FILTER

The shunt active harmonic filter (AHF) is a device that is connected in parallel to and cancels the reactive and harmonic currents from a nonlinear load. The resulting total current drawn from the ac main is sinusoidal. Ideally, the APF needs to generate just enough reactive and harmonic current to compensate the nonlinear loads in the line. In an AHF depicted in Fig. 1, a current controlled voltage source inverter is used to generate the compensating current ( $I_c$ ) and is injected into the utility power source grid. This cancels the harmonic components drawn by the non-linear load and keeps the utility line current ( $i_s$ ) sinusoidal. A method is used for instantaneous current harmonics detection in active power filter is synchronous d-q reference frame theory separating successive harmonic components. SAHF consists of voltage source converter with DC link capacitor which generates compensating current with 180 phase opposition and injects at the Point of Common Coupling (PCC) in the grid, so as to cancel out the current harmonics caused by the non-linear load. SAHF compensates current harmonics by injecting equal-but opposite harmonic compensating current. The components of harmonic currents contained in the load current are cancelled by the effect of the active filter, and



the source current remains sinusoidal and in phase with the respective phase to neutral voltage. With an appropriate control scheme, the active power filter can also compensate the load power factor [7].

### V. CONTROL STRATEGY OF SAHF

As the most integral component in ensuring optimal performance of the SAHF, it is important to select the most suitable control strategy based on the applications. For this paper, the SAHF is applied at 33kV MV line. Therefore, the control strategy applied is aimed to reduce the computational complexity and improve response time for harmonic cancellation which will ultimately lower the cost of SAHF. The proposed control strategy is named DC-linked reference current compensation method. Fig. 4 shows the topology of the three-phase SAHF with the proposed control strategy. A general overview of the control strategy consists of three core elements which are reference current generation, DC-link feedback control, and current control and gate signal generation. Lack of any of the core elements will result in the SAHF not functioning as required.

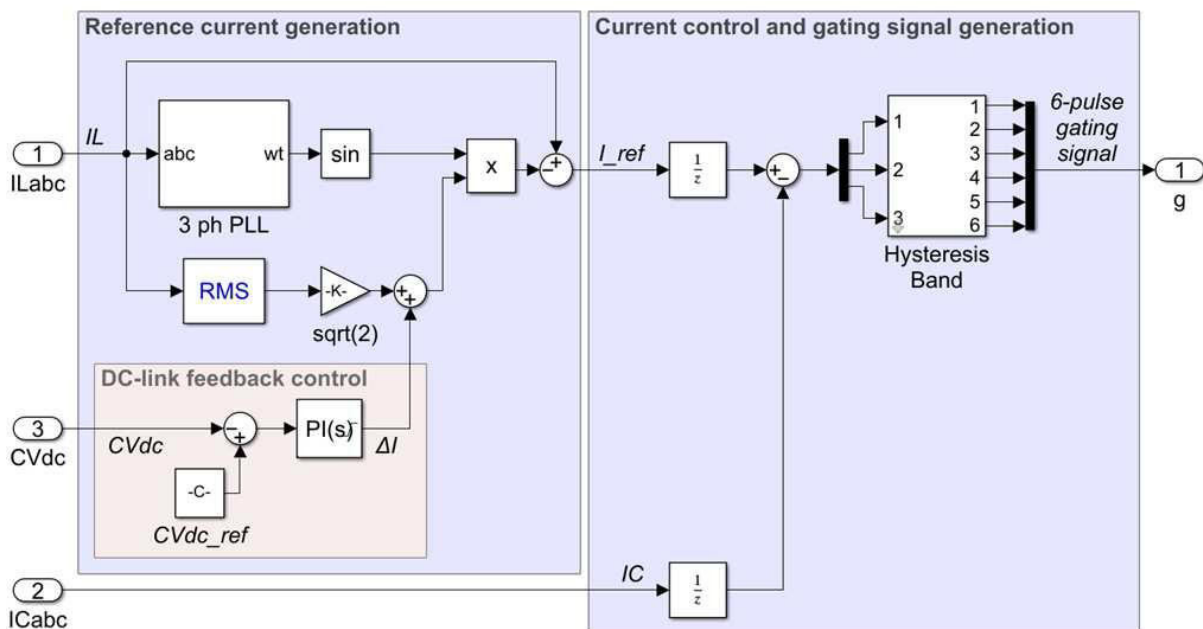


Fig. 3. Proposed DC-linked reference current compensation control model

The reference current generation stage is the most important aspect of the SAHF control algorithm as reference current ( $I_{ref}$ ) will directly impact the accuracy of harmonic cancellation. Time domain-based methods are chosen as they have a fast response time and use less computational power. A proposed reference current generation method, named DC-linked peak compensation control, is implemented due to being less mathematically complex which improves the response time. When the error signal between  $I_{ref}$  and  $I_C$  is within the hysteresis band limit, the gating signal will remain unchanged. Inversely, the gating signal will be toggled by the hysteresis switch each time the error signal exceeded the band limit which is either from 1 to 0 or vice versa. As a result, the actual  $I_C$  generated by SAHF will resemble the  $I_{ref}$  to compensated distorted load current waveform.

### VI. RESULTS AND DISCUSSIONS

By using MATLAB/Simulink, a DC electric railway TPSS with SAHF model is created to evaluate the performance of SAHF in mitigating harmonics in load conditions. DC-linked reference current compensation method is implemented as the SAHF control strategy. The effects of increasing the magnitude of the parameters involved on SAHF performance resulted in different outcomes.  $L_f$ , HBL,  $K_p$ , and  $K_i$  are not considered in the performance analysis as  $L_f$  and HBL are fixed parameters while  $K_p$  and  $K_i$  are dependent on the filter capacitance. For filter capacitance, it has increased the SAHF settling time and reduces  $V_{pp}$ . Besides that,  $CV_{dc\_ref}$  will vary the SAHF settling time, reduce power factor, THDi, TDDi, 11th and 13th order THDi, and increase THDv. Lastly,  $I_{range}$  reduces SAHF settling time.

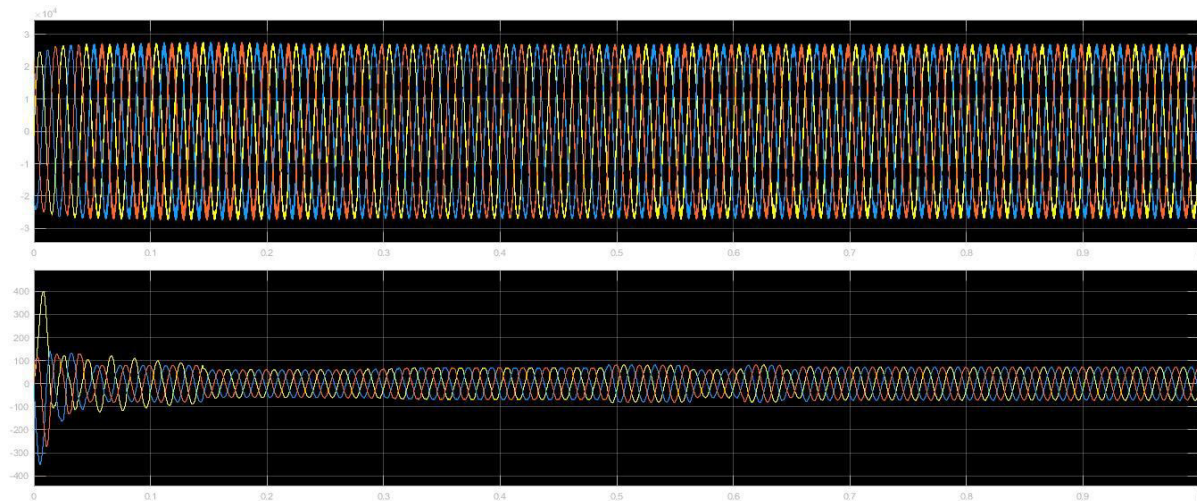


Fig 5 Source voltage and Source Current

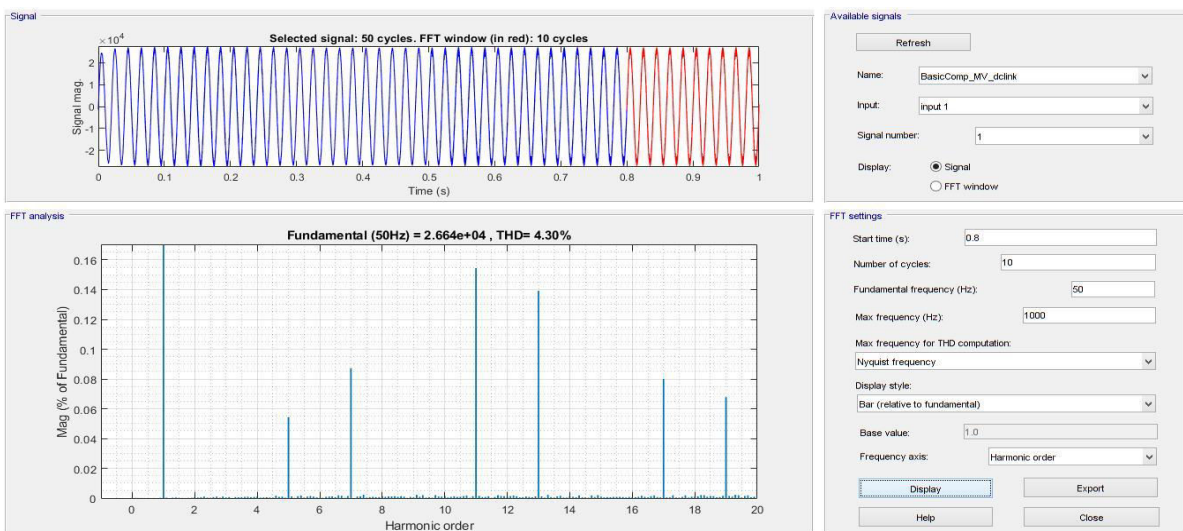


Fig.6 Harmonic spectrum of the source voltage

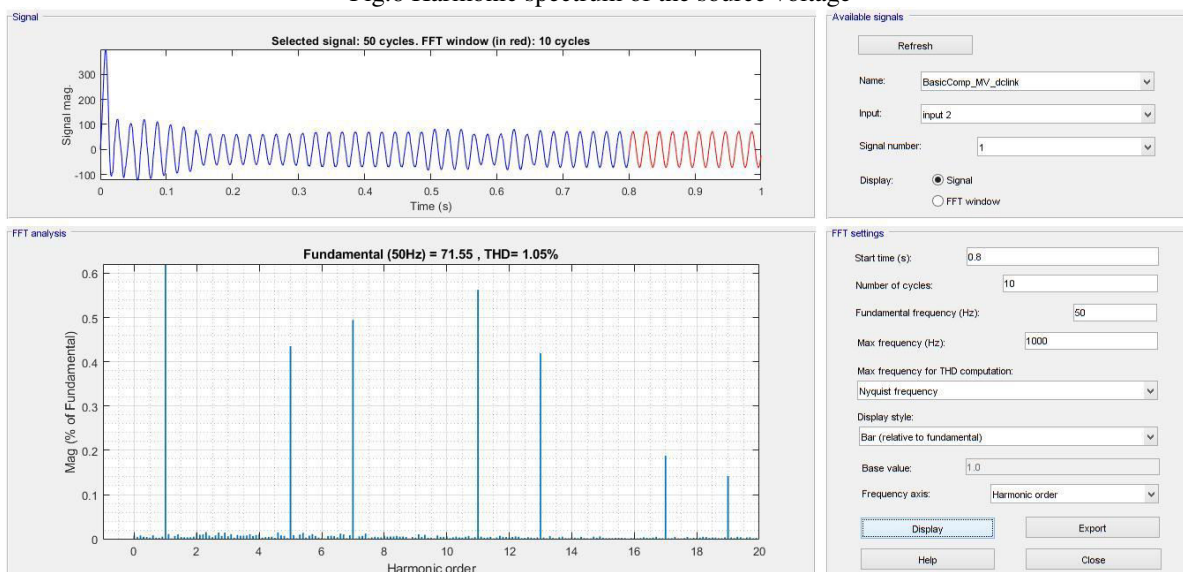


Fig.7 Harmonic spectrum of the source current

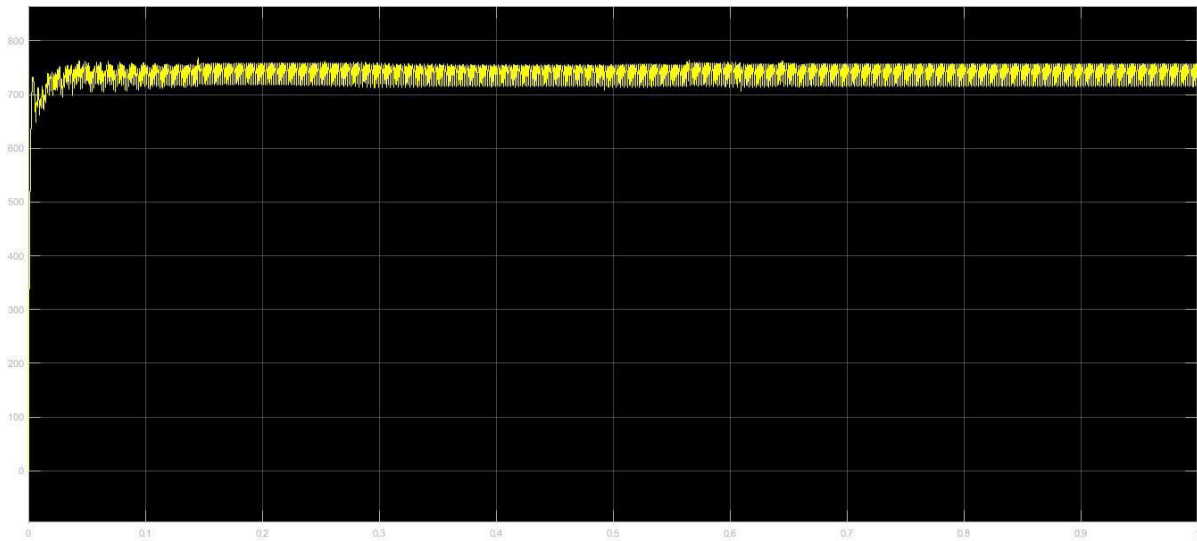


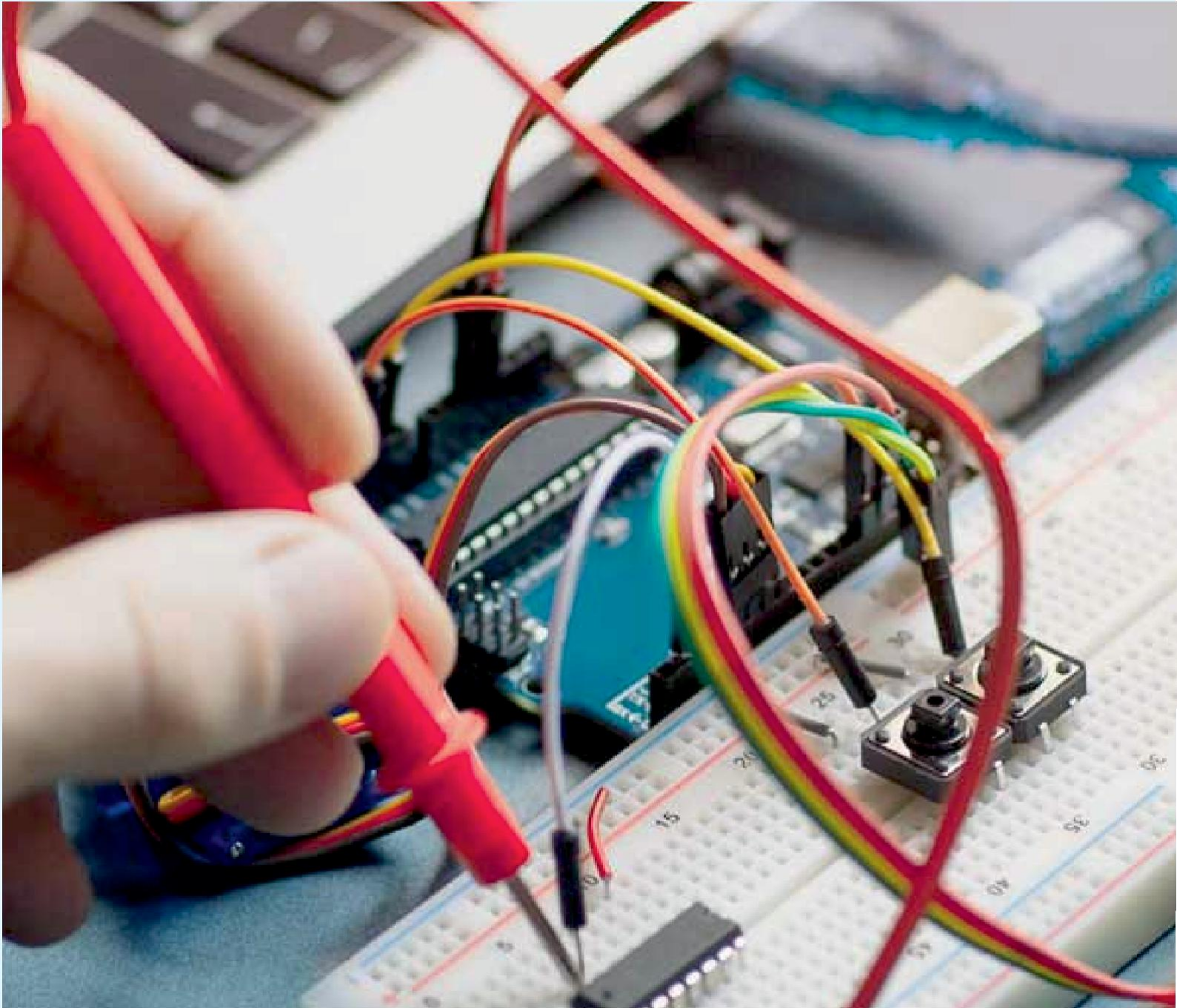
Fig 8 Load voltage

## VII. CONCLUSION

In summary, performance analysis of SAHF is carried out by evaluating the SAHF performance in mitigating the harmonic distortion present in DC electric railway TPSS system at the 33kV MV line. Prior to harmonic mitigation, 11<sup>th</sup> and 13<sup>th</sup> order harmonics are the dominant harmonics present in the affected system. The SAHF control strategy, named DC-linked reference current compensation method, is proposed which consists of three core elements namely DC-linked peak compensation control, DC-link feedback control, and hysteresis band control.  $I_{ref}$  is generated in the DC-linked peak compensation control stage while the hysteresis band control stage will ensure the actual IC generated will be similar to the  $I_{ref}$ . DC-link feedback control improves SAHF performance by compensating switching loss. The harmonic analysis results have shown that THD<sub>v</sub>, TDD<sub>i</sub>, 11<sup>th</sup> order IHD<sub>i</sub>, and 13<sup>th</sup> order IHD<sub>i</sub> of TPSS MV line is reduced to levels lower than 4.61%, 1.03%, 0.96%, and 0.67% respectively. Therefore, the proposed control strategy is shown to be capable of mitigating harmonics at the 33kV MV line with the advantage of having a simple topology, minimal mathematical complexity, and fast response time.

## REFERENCES

- [1] F. A. A. Rahman, M. Z. A. A. Kadir, M. Osman, and U. A. U. Amirulddin, "Review of the AC Overhead Wires, the DC Third Rail and the DC Fourth Rail Transit Lines: Issues and Challenges," *IEEE Access*, vol. 8, pp. 213277–213295, 2020.
- [2] T. Soong and P. W. Lehn, "Evaluation of emerging modular multilevel converters for bess applications," *IEEE Transactions on Power Delivery*, vol. 29, no. 5, pp. 2086–2094, 2014.
- [3] P. Medina, A. Bizuayehu, J. P. Catal˜ao, E. M. Rodrigues, and J. Contreras, "Electrical energy storage systems: Technologies' state-of-the-art, techno-economic benefits and applications analysis," in *Hawaii IEEE International Conference on System Sciences*, 2014, pp. 2295–2304.
- [4] Milanés María Isabel, Cadaval Enrique Romero, González Fermín Barrero. Comparison of control strategies for shunt active power filters in three-phase four-wire systems. *IEEE Trans Power Electron* 2007;22(1):229–36.
- [5] Z. Qiu, J. Kong, and G. Chen, "A novel control approach for LCL-based shunt active power filter with high dynamic and steady-state performance," in *Proc. IEEE Power Electron. Spec. Conf.*, 2008, pp. 3306–3310.
- [6] Y. Hoon, M. A. M. Radzi, M. K. Hassan, and N. F. Mailah, "Enhanced Instantaneous Power Theory with Average Algorithm for Indirect Current Controlled Three-Level Inverter-Based Shunt Active Power Filter under Dynamic State Conditions," *Mathematical Problems in Engineering*, vol. Article ID 9682512, 12 pages, 2016.
- [7] Y. Hoon, M. A. M. Radzi, M. K. Hassan, and N. F. Mailah, "DC-Link Capacitor Voltage Regulation for Three-Phase Three-Level Inverter-Based Shunt Active Power Filter with Inverted Error Deviation Control," *Energies*, vol. 9, no. 7, 533, 2016.
- [8] M. Monfared, S. Golestan, and J. M. Guerrero, "A New Synchronous Reference Frame-Based Method for Single-Phase Shunt Active Power Filters," *Journal of Power Electronics*, vol. 13, no. 4, pp. 692–700, 2013.
- [9] N. Eskandarian, Y. A. Beromi, and S. Farhangi, "Improvement of Dynamic Behavior of Shunt Active Power Filter Using Fuzzy Instantaneous Power Theory," *Journal of Power Electronics*, vol. 14, no. 6, pp. 1303–1313, November 2014.



INNO  SPACE  
SJIF Scientific Journal Impact Factor

Impact Factor: 8.18

 **doi**<sup>®</sup>  
**cross** **ref**

 **INTERNATIONAL  
STANDARD  
SERIAL  
NUMBER  
INDIA**



# International Journal of Advanced Research

in Electrical, Electronics and Instrumentation Engineering

 9940 572 462  6381 907 438  [ijareeie@gmail.com](mailto:ijareeie@gmail.com)



[www.ijareeie.com](http://www.ijareeie.com)

Scan to save the contact details